

Corrections to 6x86MX Data Book

May 30, 1997

Pages 2-78, 2-79, 3-54, 3-55 and 3-56

WAS:

SMACT#

NOW:

SMIACT#

Page 2-79 6th Paragraph (Nested SMI)

WAS:

Cyrix enhanced

NOW:

Cyrix Enhanced

Page 2-79 9th paragraph, 7th line in paragraph

WAS:

CPU will not to inadvertently receive and service

NOW:

CPU will not inadvertently receive and service

Page 5-6, Table 5-3, second line:

WAS:

A1 25.15 28.47 0.990 0.121

NOW:

A1 2.51 3.07 0.099 0.121
